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24309 7590 09/15/2004

XILINX, INC  
ATTN: LEGAL DEPARTMENT  
2100 LOGIC DR  
SAN JOSE, CA 95124

12/08/2004 GWORDF2 00000077 240040 10787332

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Pat. Slaback (Depositor's name)  
*Pat Slaback* (Signature)  
December 6, 2004 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10787,332

02/26/2004

Jan L. de Jong

X-1010-2D US

3967

TITLE OF INVENTION: STRUCTURES AND METHODS FOR DETERMINING THE EFFECTS OF HIGH STRESS CURRENTS ON CONDUCTING LAYERS AND CONTACTS IN INTEGRATED CIRCUITS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
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nonprovisional

NO

\$1330

\$0

\$1330

12/15/2004

\$1370

EXAMINER	ART UNIT	CLASS-SUBCLASS
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NGUYEN, VINCENT Q

2858

324-719000

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☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.

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(1) the names of up to 3 registered patent attorneys or agents OR, alternatively,

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1 Lois D. Cartier

2

3

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

XILINX, INC.

San Jose, California 95124

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

4a. The following fee(s) are enclosed:

☒ Issue Fee☐ Publication Fee (No small entity discount permitted)☐ Advance Order - # of Copies

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☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.☒ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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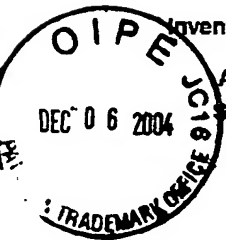
Authorized Signature

Date December 6, 2004Typed or printed name Keith A. ChanrooRegistration No. 36,480

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Inventor(s): Jan L. de Jong et al.

Assignee: **XILINX, INC.**

Serial No.: 10/787,332

conf.no. 3967

Filed: February 26, 2004

Title: Structures and Methods for Determining the Effects of High Stress Currents on  
Conducting Layers and Contacts in Integrated Circuits

Docket No.: **X-1010-2D US**

Enclosed: Return Receipt Postcard  
Fee Transmittal - Part B

Date: December 6, 2004

**VIA FACSIMILE 703-746-4000**

Atty/Sec: KAC/pbs